

February 22, 2024

## To Whom It May Concern:

Our Mercury LP Intelligent Controllers and Series 3 MR Serial Input/Output (SIO) Modules use both volatile and non-volatile memory. The requested memory characteristics are listed below:

| Memory type    | Туре               | User modifiable | Function or use    | Process to clear        |
|----------------|--------------------|-----------------|--------------------|-------------------------|
| Flash          | Internal, non-     | Υ               | Operating System,  | Configuration data is   |
|                | volatile           |                 | Program storage,   | cleared with API reset  |
|                |                    |                 | configuration data | command or bulk         |
|                |                    |                 |                    | erase function via      |
|                |                    |                 |                    | physical switch         |
|                |                    |                 |                    | interaction or API      |
|                |                    |                 |                    | command (LP-only)       |
| RAM            | Internal, volatile | N               | Contains boot      | Power Off system        |
|                |                    |                 | parameters and     |                         |
|                |                    |                 | operating code     |                         |
| EEPROM         | Internal, non-     | Υ               | Manufacturing      | Communication           |
|                | volatile           |                 | data,              | configuration is        |
|                |                    |                 | Communication      | cleared with bulk erase |
|                |                    |                 | configuration      | function (via physical  |
|                |                    |                 |                    | switch interaction or   |
|                |                    |                 |                    | API command)            |
| SRAM (LP-only) | Internal, non-     | Υ               | Temporary card     | Power Off system with   |
|                | volatile           |                 | storage,           | coin cell battery       |
|                |                    |                 | transaction        | removed/disconnected    |
|                |                    |                 | storage            | (decay occurs           |
|                |                    |                 |                    | generally within 30     |
|                |                    |                 |                    | seconds at room         |
|                |                    |                 |                    | temperature)            |

Regards,

Damon Dageenakis **HID Global** 

**Product Management**